



K. Sambal

Avinashilingam Institute for Home Science and Higher Education for Women
(Deemed to be University Estd. u/s 3 of UGC Act 1956, Category 'A' by MHRD)
Re-accredited with 'A++' Grade by NAAC. Recognised by UGC Under Section 12B
Coimbatore - 641 043, Tamil Nadu, India

Continuous Internal Assessment II – October 2025
V SEMESTER

Class : III UG (2019 & 2022 Repeater)
Major: Computer Applications

Time : 2 Hours
Max Marks: 60

18BCAC20/ 21BCAC20 Software Engineering

Course Outcomes:

CO1:Acquire strong fundamental knowledge in software engineering.

CO2:Ability to apply software engineering-principles, techniques, tools and practices

CO3:Effectively demonstrate competence in communication, planning, analysis, design, construction, testing and deployment

CO4:Adapt to new emerging technologies and methodologies

CO5:Cope up with software quality standards

Part – A

6x 1=6

Choose the correct answer

1. Which of the following is NOT a phase of Requirement Engineering? CO3 K1
 - a. Requirement elicitation
 - b. Requirement analysis
 - c. Requirement validation
 - d. Requirement coding
2. What is the main goal of requirement analysis? CO3 K2
 - a. Writing code directly
 - b. Understanding and documenting what the system should do
 - c. Testing the software
 - d. Designing the database
3. What is abstraction in software engineering? CO4 K3
 - a. Hiding implementation details and showing only essential features
 - b. Writing complex code
 - c. Ignoring user requirements
 - d. Making the system slower
4. High cohesion in a module means CO4 K2
 - a. All parts of the module are related to a single task
 - b. The module performs many unrelated tasks
 - c. It depends heavily on other modules
 - d. It is harder to maintain
5. Which of the following best describes the difference between verification and validation? CO5 K2
 - a. Verification focuses on whether the software does what the user wants, while validation ensures that the product was built correctly.
 - b. Verification checks if the product is being built correctly, while validation checks if the right product is being built.
 - c. Verification is done after deployment, while validation is done during development.
 - d. Verification is related to performance testing, and validation is related to security testing
6. In stress testing, what is typically stressed? CO5 K3
 - a. The database schema
 - b. The network and CPU usage
 - c. The user interface responsiveness
 - d. The project documentation

Part- B

3x6=18

Answer ALL Questions

Each answer should not exceed 400 words or two pages

7. a. Write in detail about Requirement analysis. CO4 K1
- (or)
7. b. Briefly explain the Data Modeling Concepts CO4 K2

9. a. Why validation and verification is required in testing. Explain with example (or) CO5 K3

9. b. How to conduct performance and recovery testing? Explain with example. CO5 K1

Part-C

3x12=36

Answer ALL questions

Each answer should not exceed 800 words or four pages

10. a. Discuss about Initiating the Requirements Engineering process. CO3 K2
(or)

10. b. Describe Analysis Modeling and its Approaches. CO4 K2

11. a. Discuss in brief about A Strategic approach to Software Testing. CO4 K1
(or)

11. b. Elaborate software architecture in detail. CO4 K3

12. a. Explain in brief about the testing strategies for conventional software. CO5 K2
(or)

12. b. Write in detail about white box, black box and control structure testing. CO5 K1

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Staff-In-Charge: Dr.D.Ambika